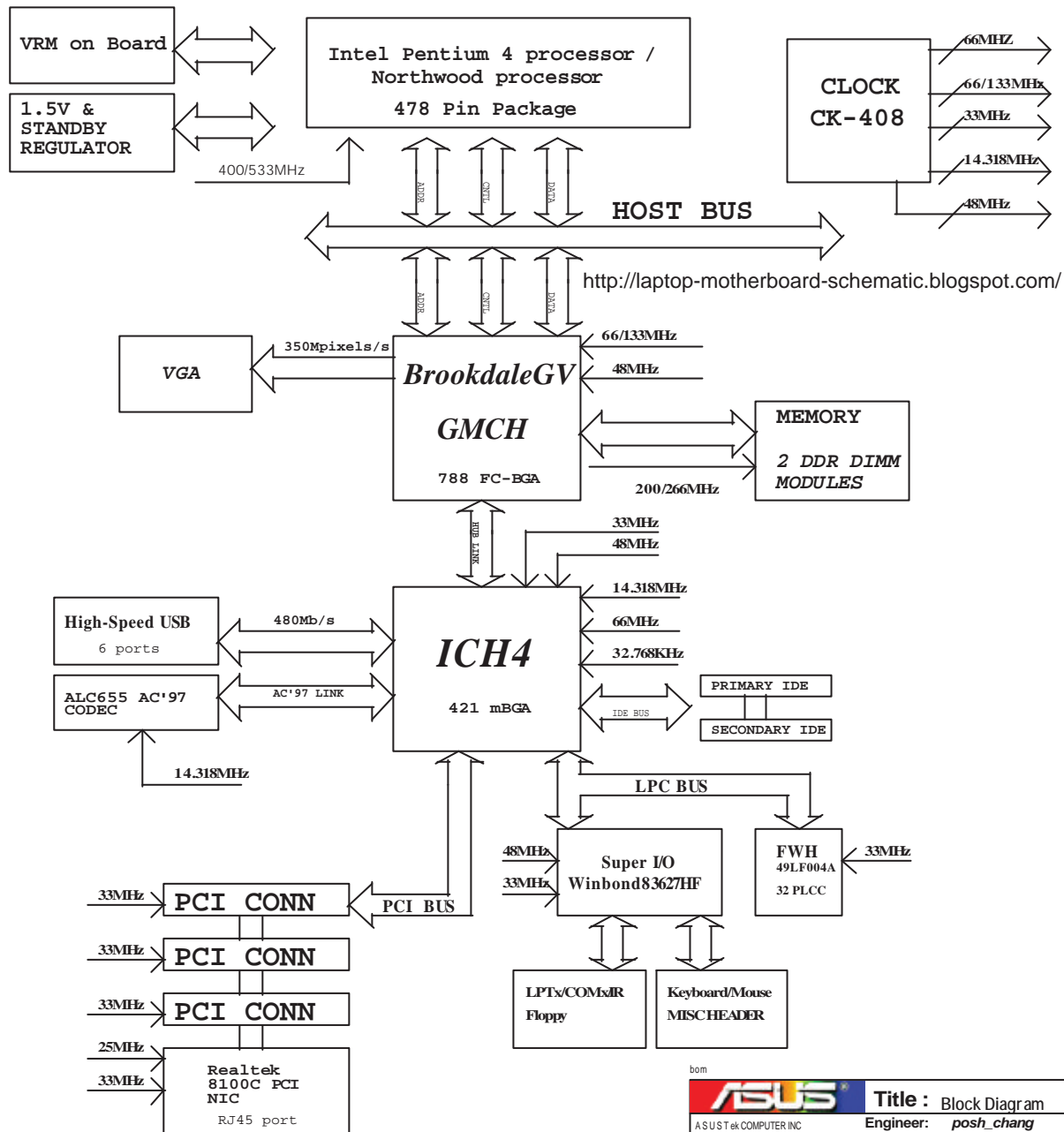
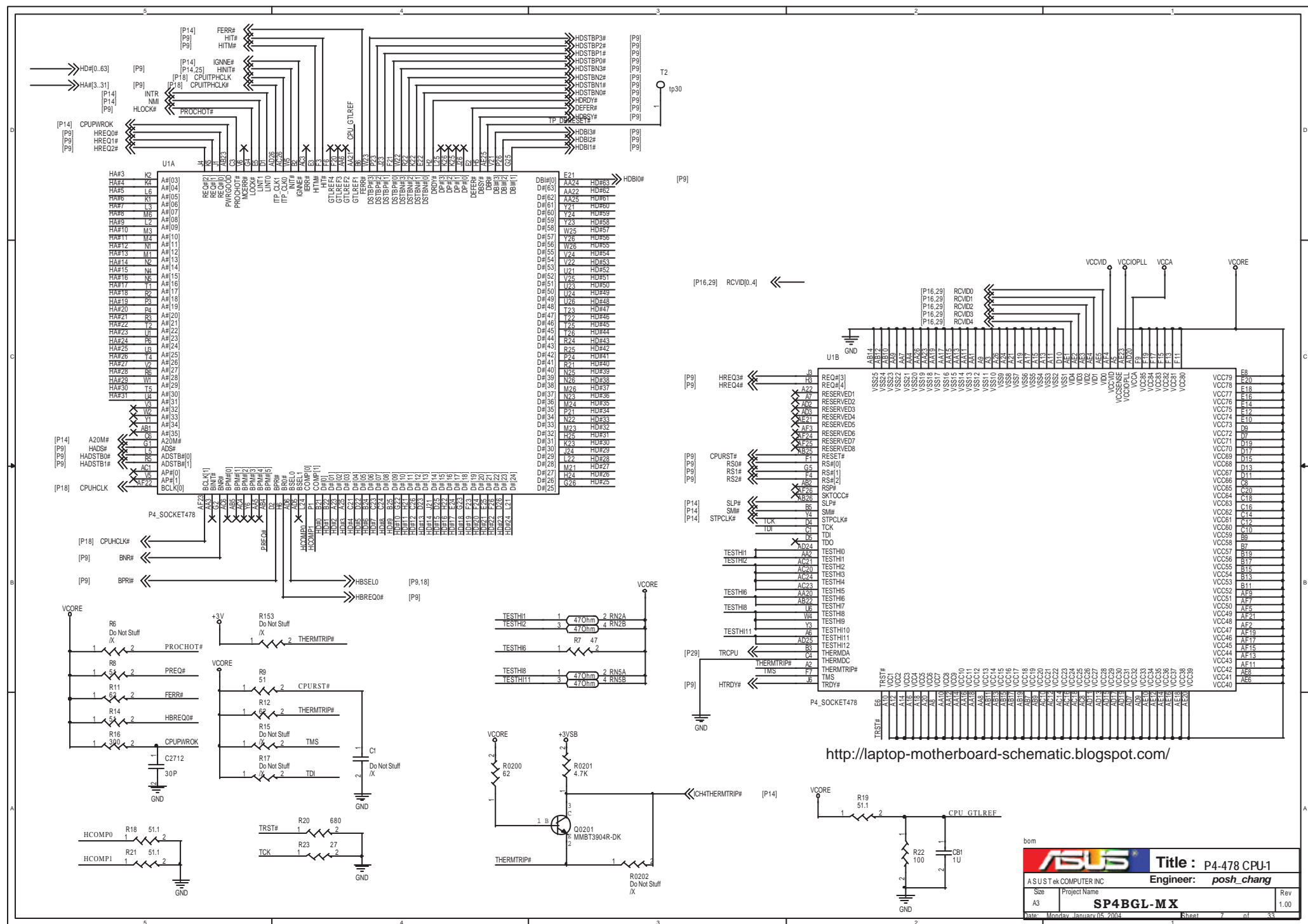


# SP4BGV-MX

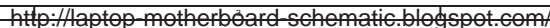
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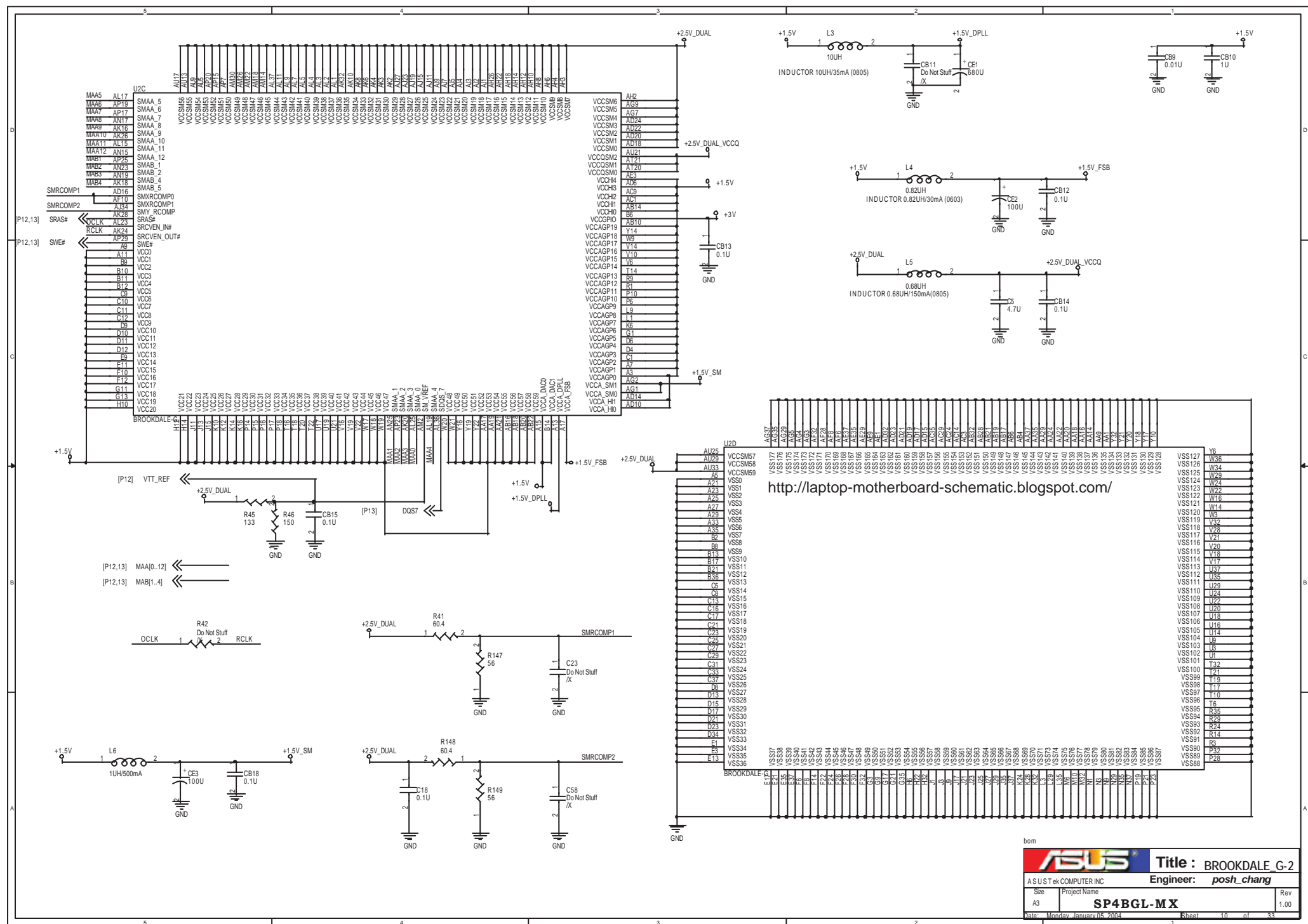
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01	Block Diagram
02	Socket478-1
03	Socket478-2
04	Brookdale-G -1
05	Brookdale-G -2
06	Brookdale-G -3
07	DDR DIMM Socket
08	DDR Termination
09	ICH4-1
10	ICH4-2
11	VRM on Board
12	SB,DUAL PWR,VTT DDR
13	CLOCK Gen.
14	AGP SLOT
15	Audio Codec ALC655
16	Audio connector
17	PCI SLOT 1 & 2
18	PCI SLOT 3
19	IDE Connector / VGA port
20	FWH
21	USB 2.0 Connector
22	RTL8100C PCI LAN
23	+1.5V,+1.5VSB,+2.5VDUAL
24	Winbond 83627
25	Winbond 83627-1
26	FRONT PANEL / BUZZER
27	ATX POWER

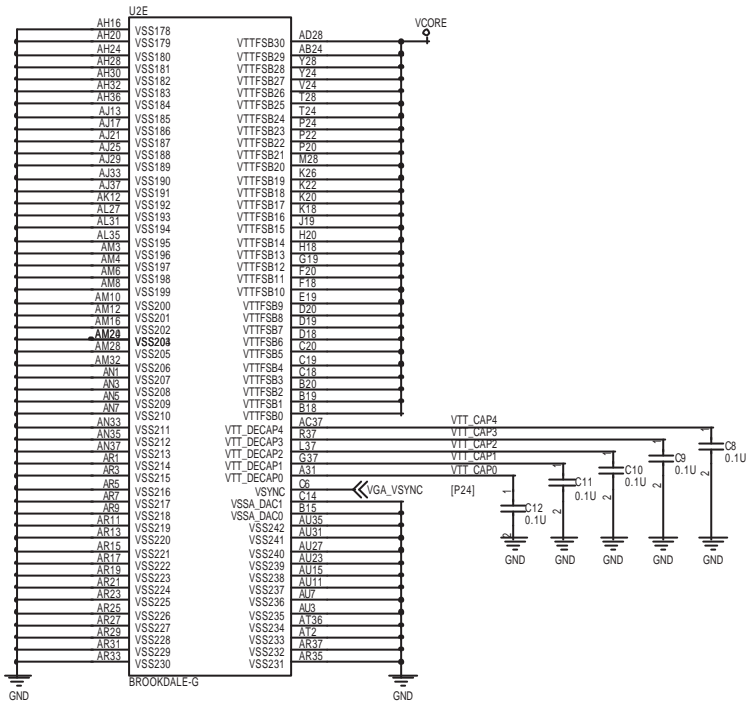




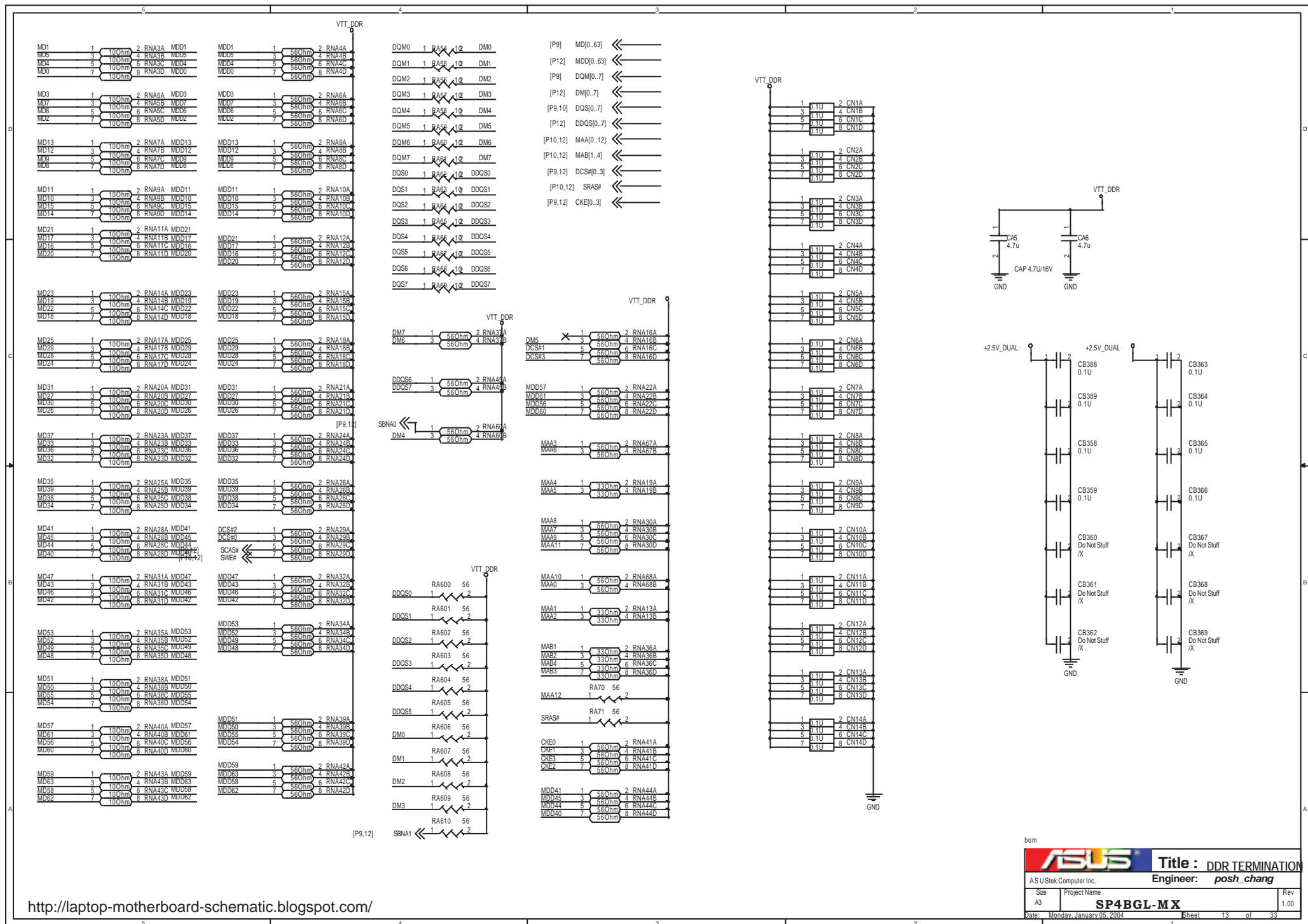




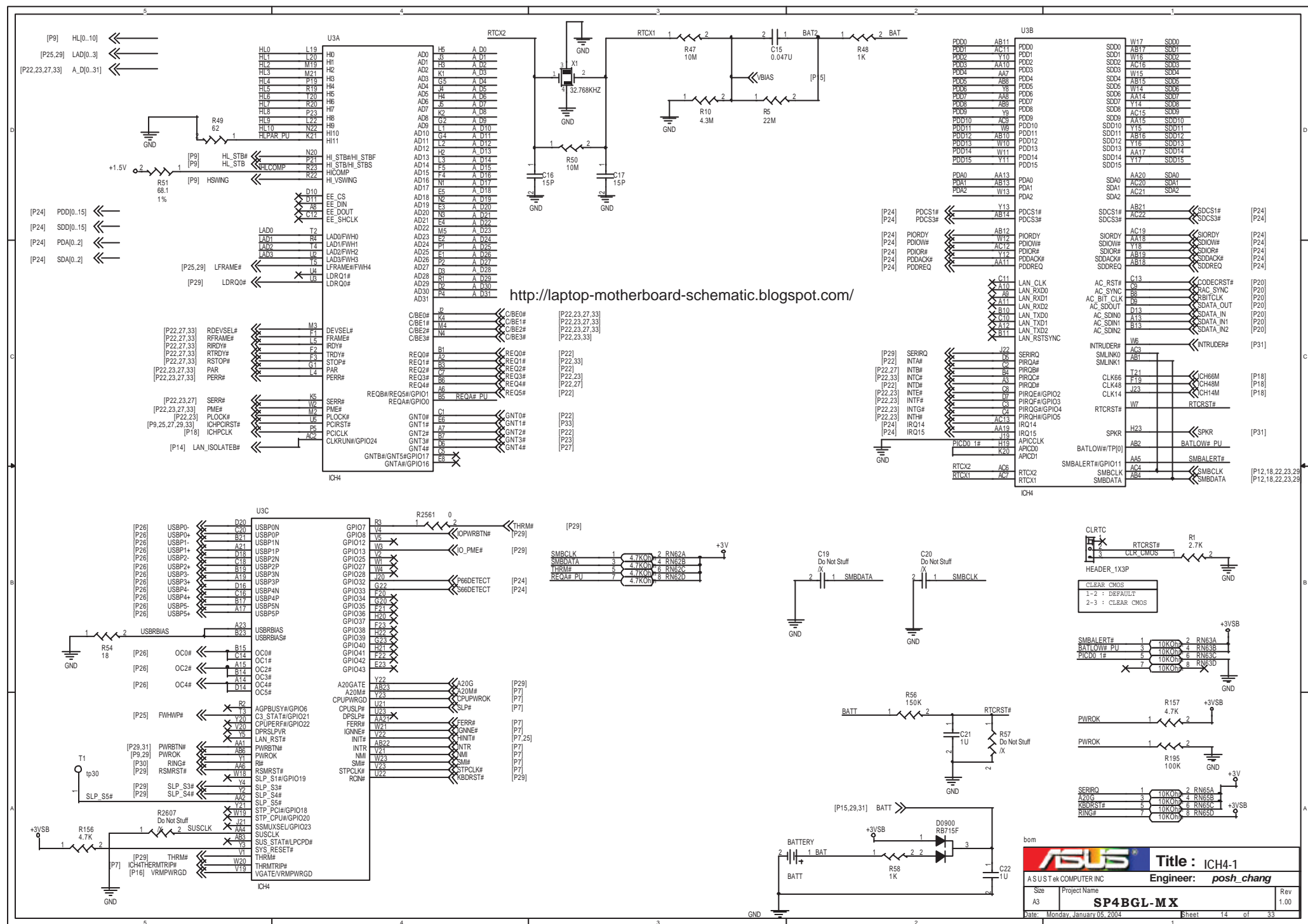






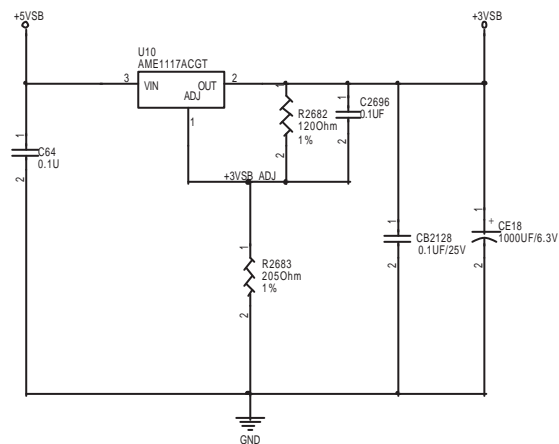
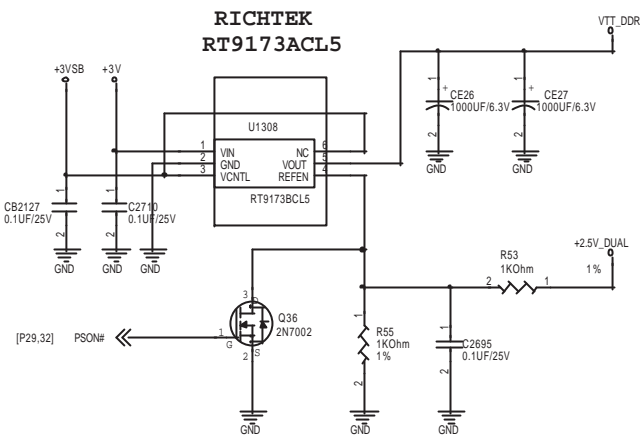
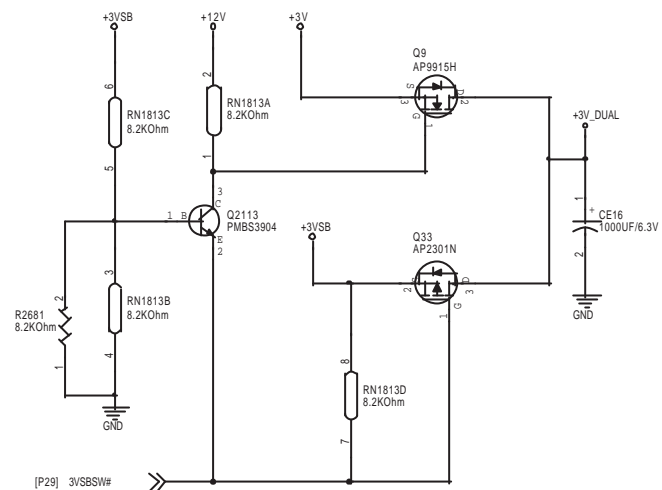








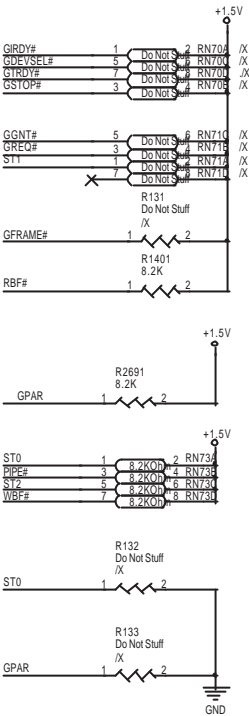
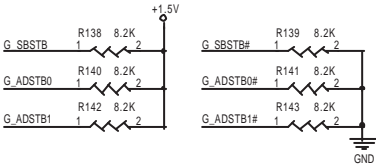
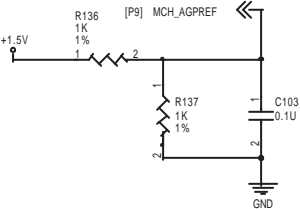
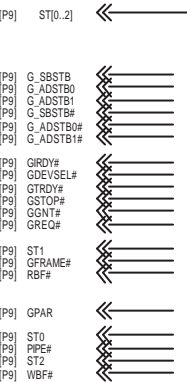


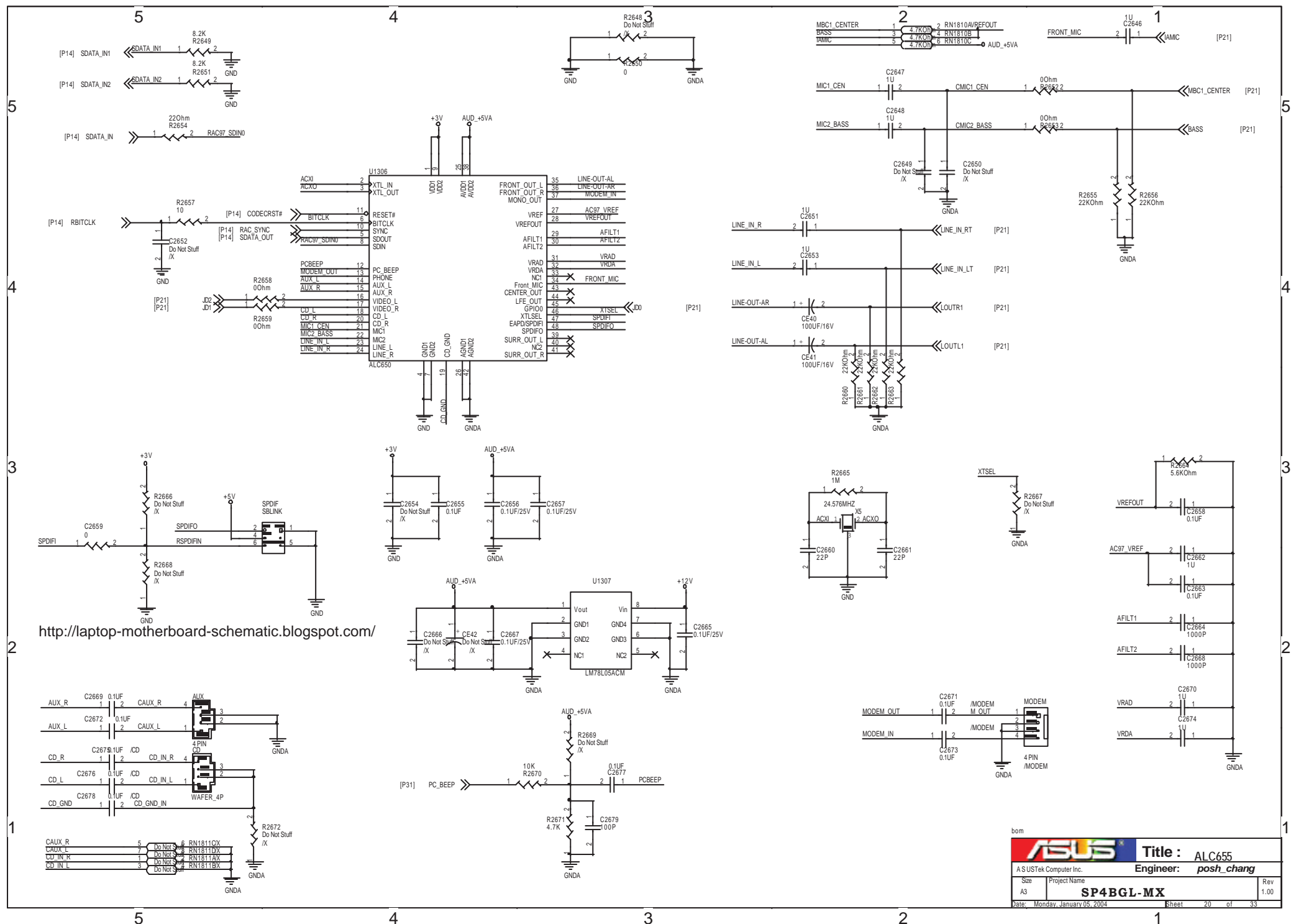


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<b>ASUS</b>		<b>Title : DUAL_POWER</b>	
ASUSTek COMPUTER INC		Engineer: <i>posh_chang</i>	
Size	Project Name		Rev
A3	<b>SP4BGL-MX</b>		1.00
Date: Monday, January 05, 2004		Sheet	17 of 33

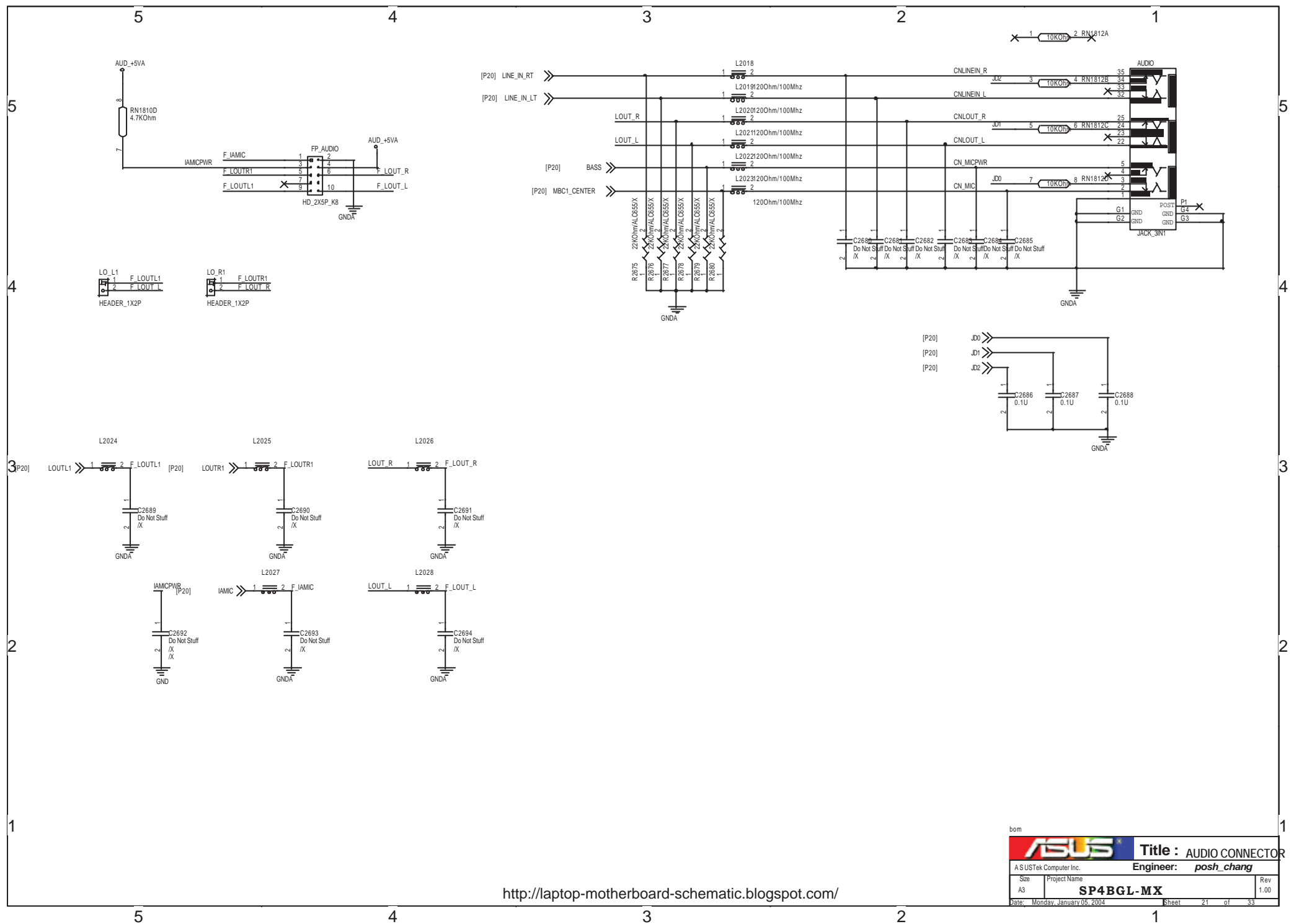




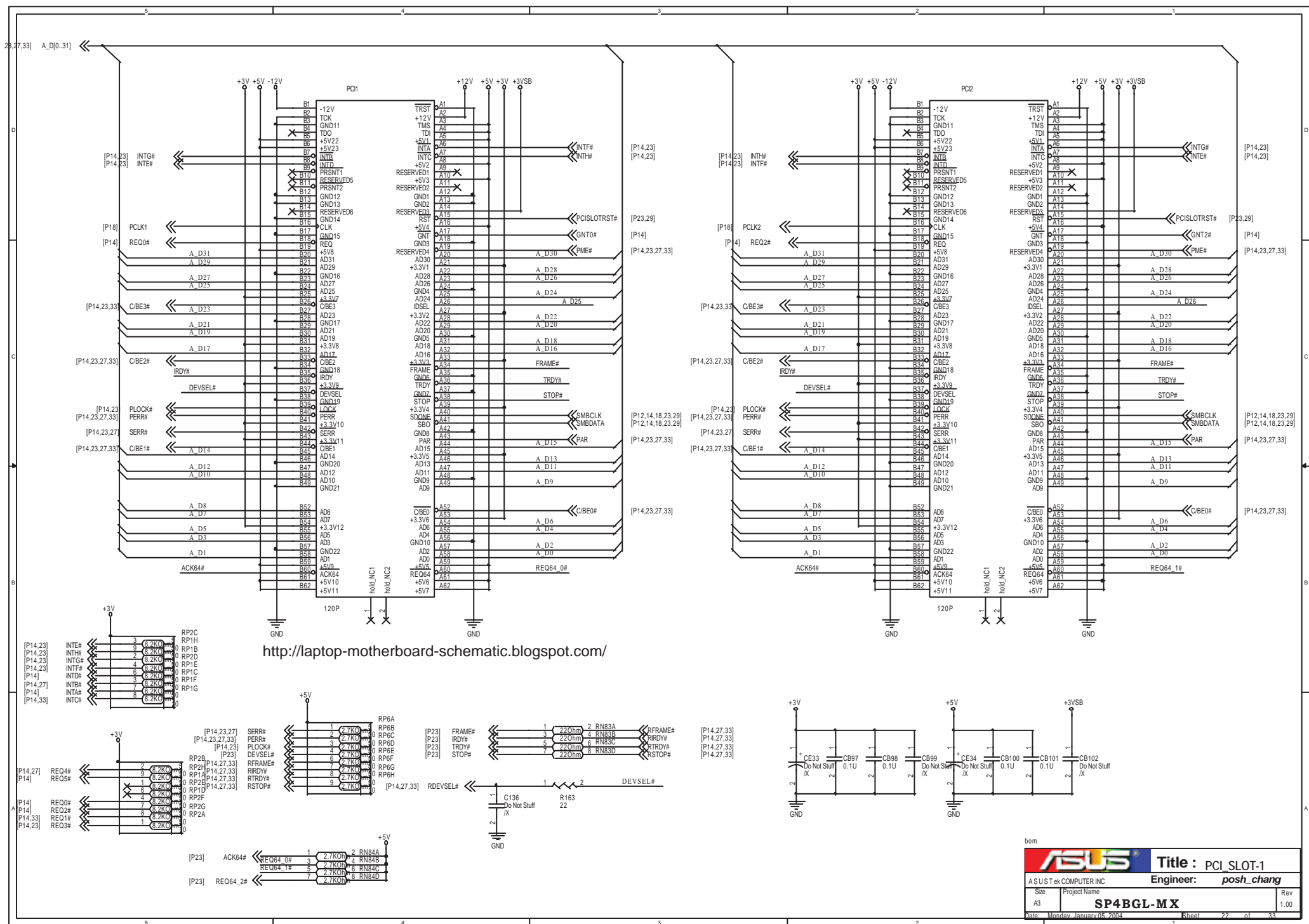


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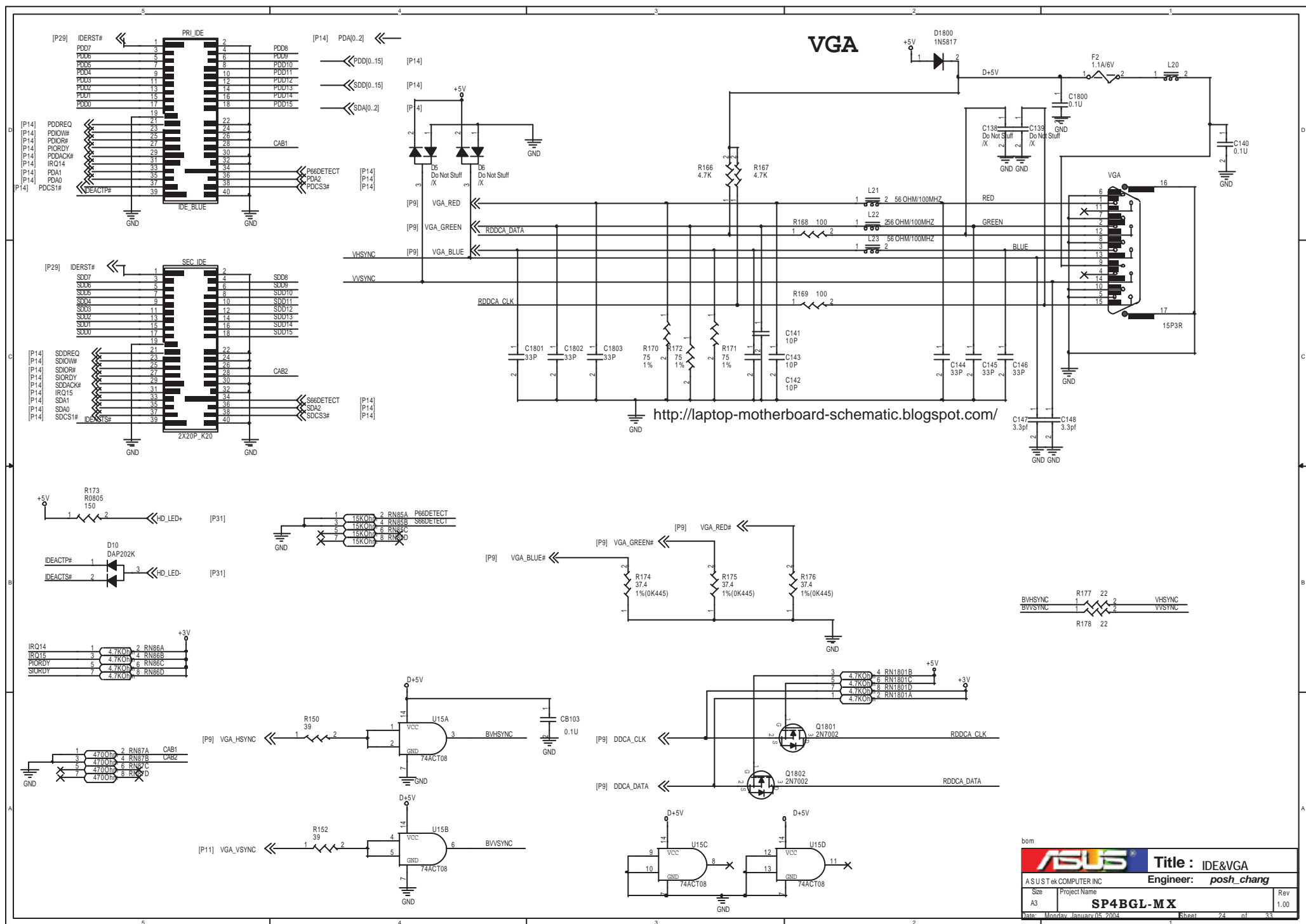
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ASUSTek Computer Inc.		Engineer: posh_chang	
Size	Project Name	Rev	
A3	SP4BGL-MX	1.00	
Date: Monday, January 05, 2004		Sheet 20 of 33	



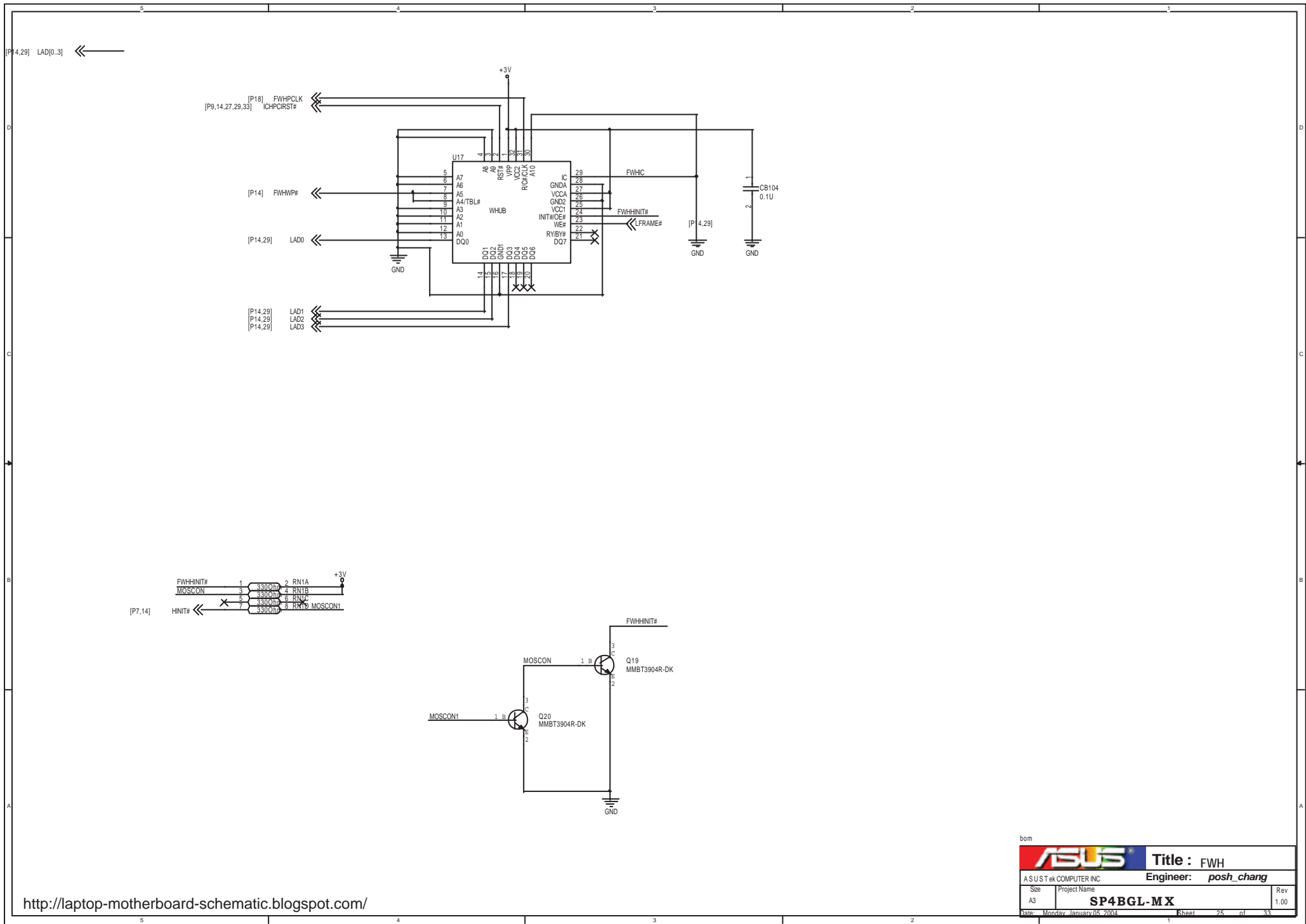


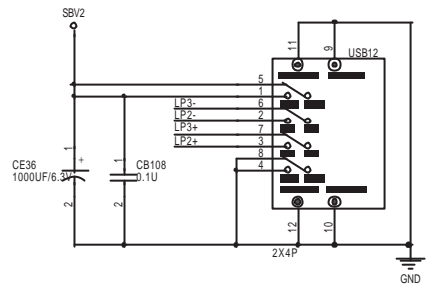
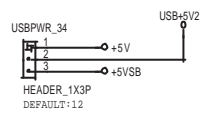
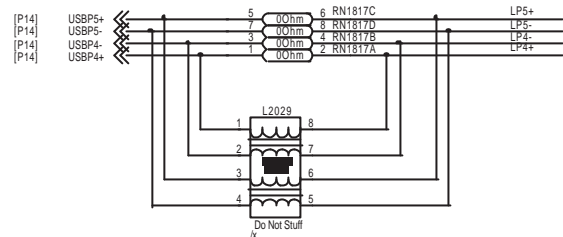
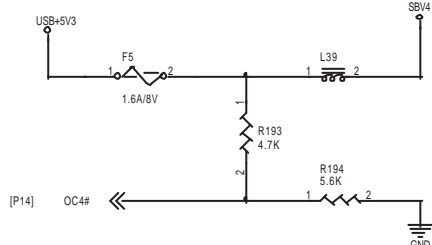
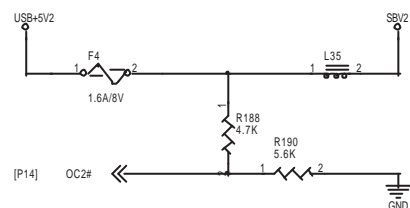
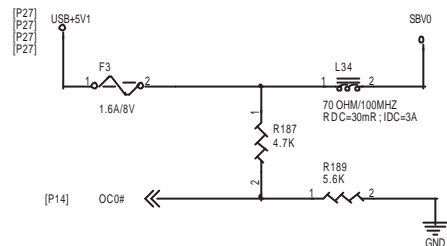
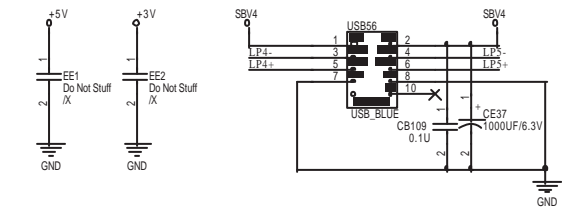
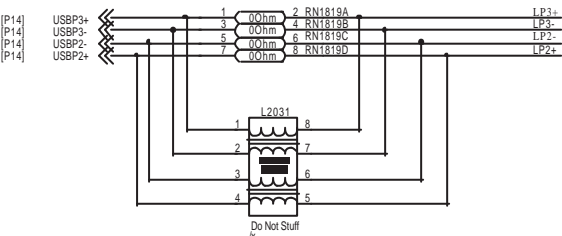
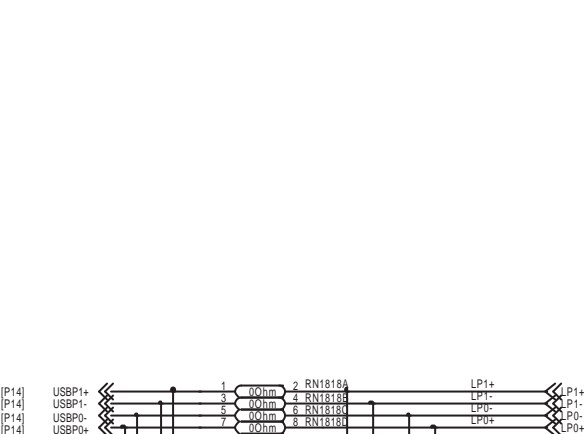






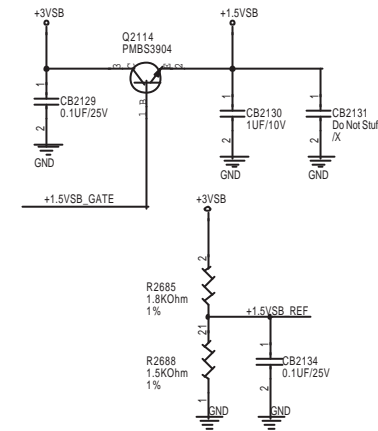
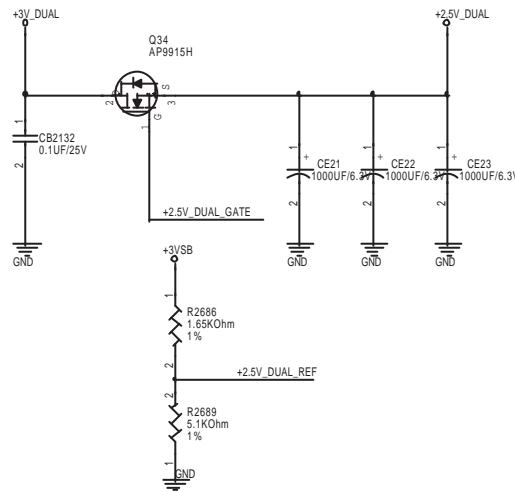
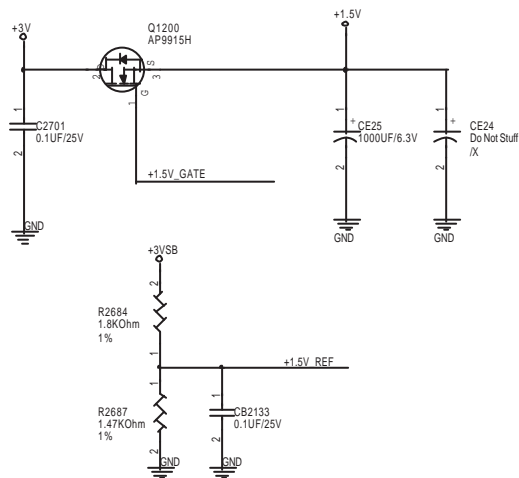
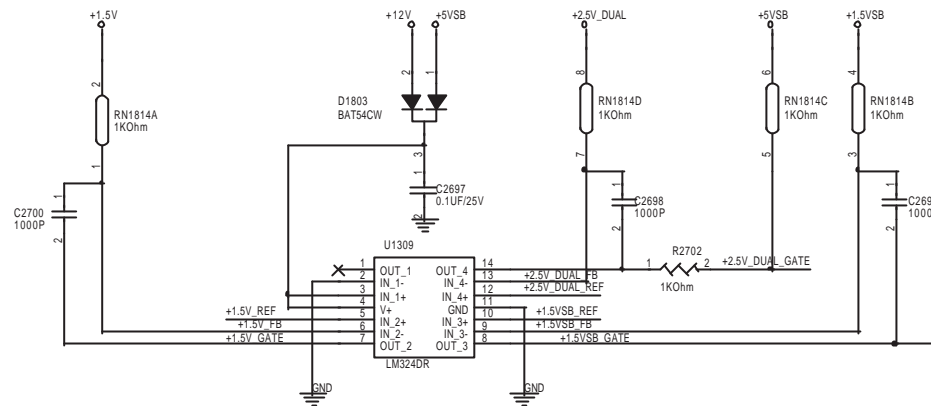
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ASUSTek COMPUTER INC		Engineer: posh_chang	
Size	Project Name	Rev	
A3	SP4BGL-MX	1.00	
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ASUS		Title : KINNERETH & USB	
ASUSTek COMPUTER INC		Engineer: posh_chang	
Size	Project Name	Rev	
A3	SP4BGL-MX	1.00	
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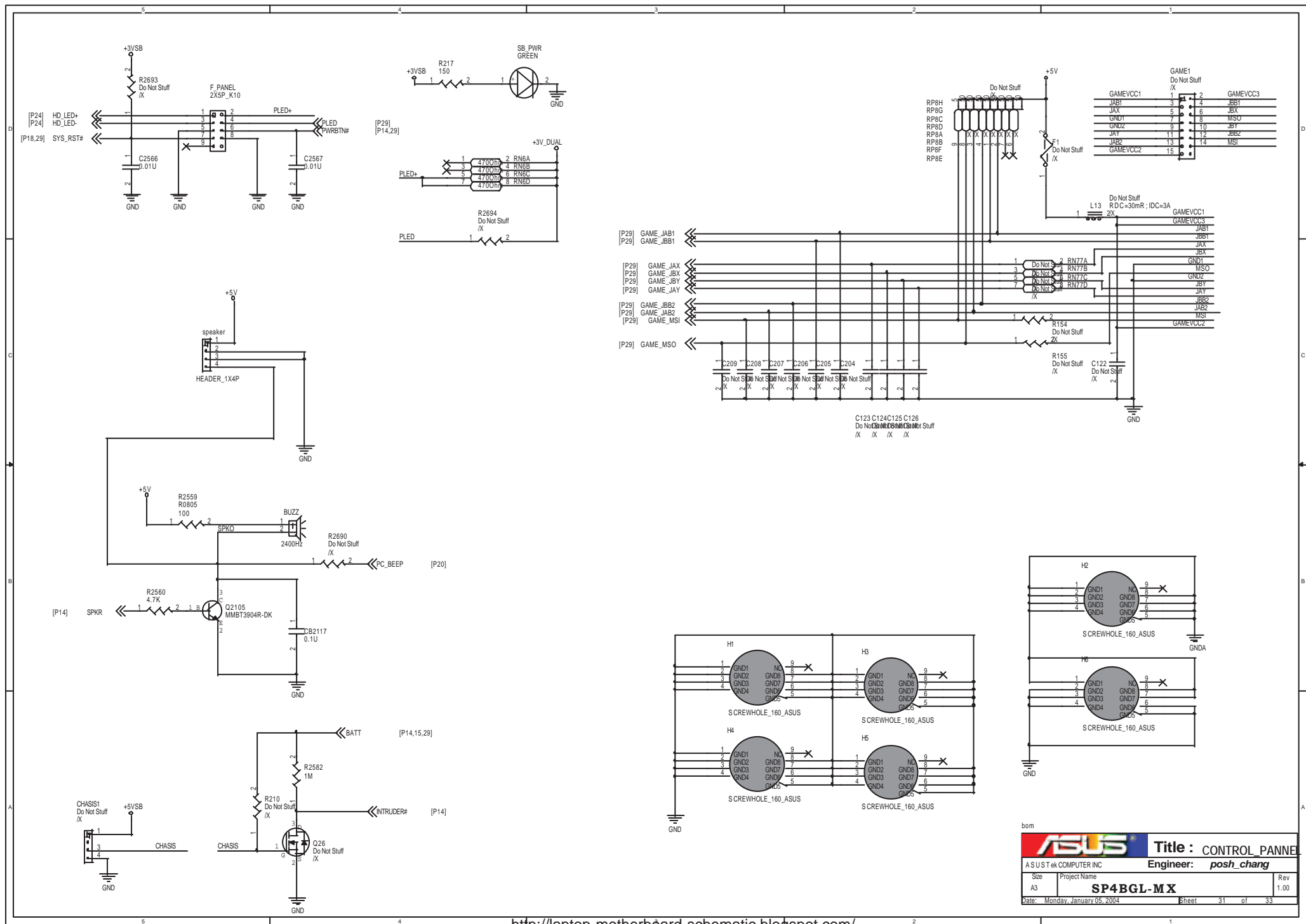


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ASUSTek COMPUTER INC		Engineer: posh_chang		Rev 1.00	
Size A3	Project Name		SP4BGL-M X		
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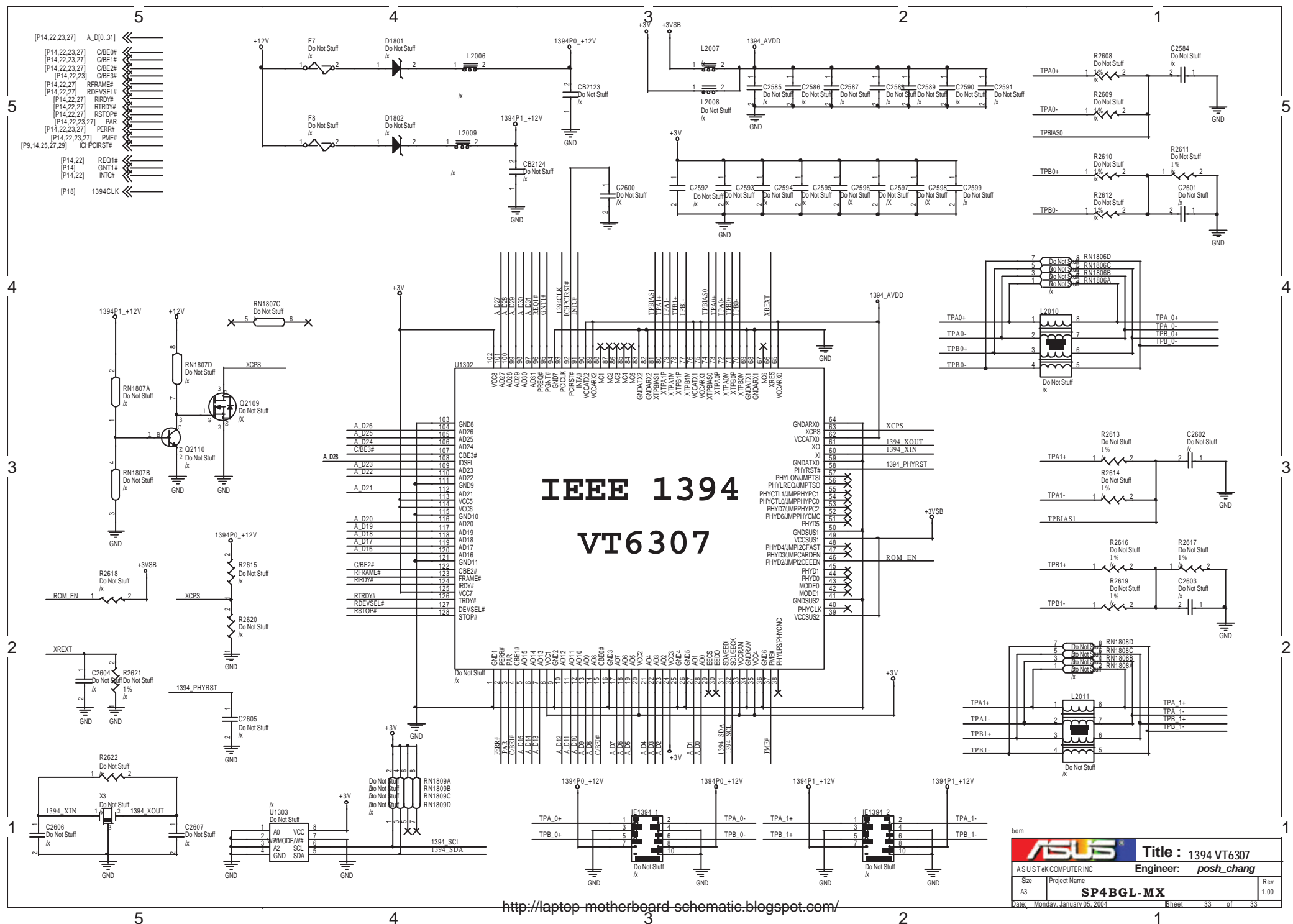




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<b>ASUS</b>		<b>Title : CONTROL_PANNE</b>	
ASUSTek COMPUTER INC		Engineer: <b>posh_chang</b>	
Size A3	Project Name <b>SP4BGL-MX</b>	Rev 1.00	
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<b>ASUS</b>		Title : 1394 VT6307	
ASUSTek COMPUTER INC		Engineer: posh_chang	
Size	Project Name	SP4BGL-MX	
A3		Rev 1.00	
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